

Applicant: Sung-We Cho

Serial No. ~~not yet assigned~~

16/674847

Filing Date: September 29, 2003

Group: ~~not yet assigned~~

2816

Title: MTCMOS FLIP-FLOP CIRCUIT CAPABLE OF RETAINING DATA IN SLEEP MODE

INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
----------------------------	------------	----------------------------------	-----------------------------	-------------	--------------	----------------------------

\_\_\_\_\_

FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
----------------------------	------------	----------------------------------	-----------------------------------	----------------	-------------

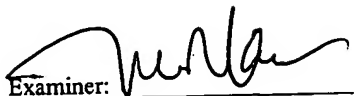
\_\_\_\_\_

OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
----------------------------	------------	--

 \_\_\_\_\_

IEEE JOURNAL OF SOLID STATE CIRCUIT, Vol.32, No.6, Satoshi Shigematsu, 1997, entitled A 1-V High-Speed MTCMOS Circuit Scheme for Power-Down Application Circuits.

Examiner: 

Date Considered: 8/31/2004